Building the Batteryless Computing Community through Access to Education, Testbeds, and Tools

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A growing community for a smaller, sustainable loT

Batteryless Challenges

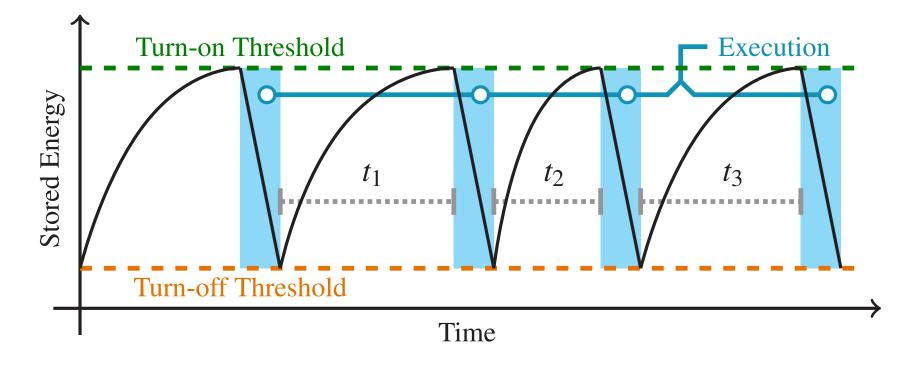
unpredictable & volatile low power, few resources frequent power failures timekeeping and timeliness

A decade of growth

~ 5 people → hundreds of researchers 893+ papers — most in the last 5 years

Spanning many areas: programming

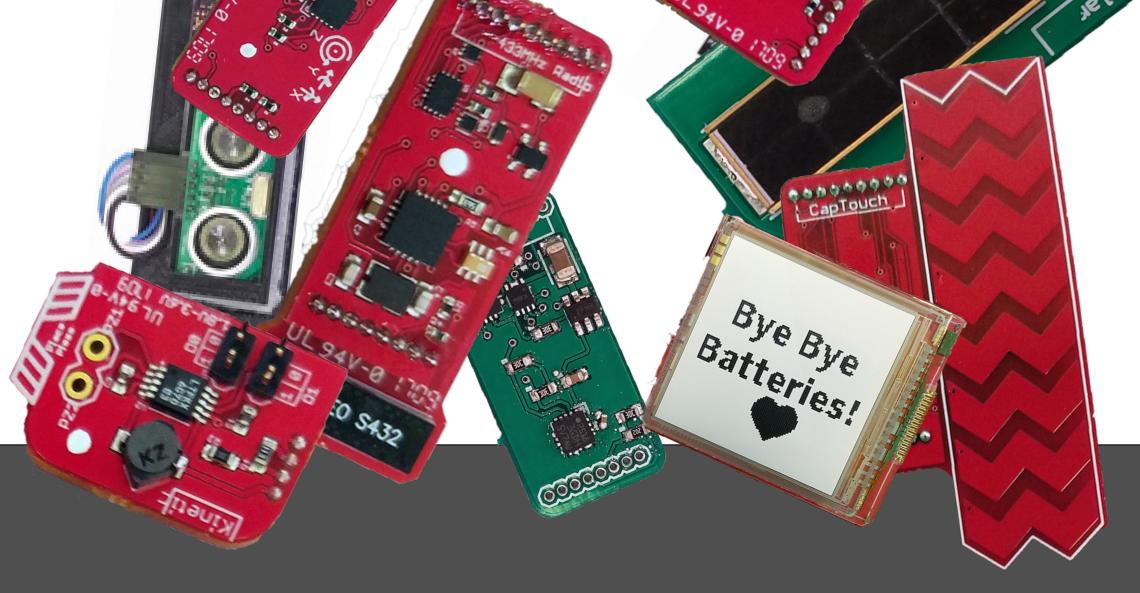
consistency and forward progress



languages, operating systems, networks, architecture, embedded systems, sensors, HCI

Conferences: ASPLOS, IMWUT, SenSys, IPSN, OOPSLA, and more.

So many new tools, techniques and artifacts!



Batteryless Computing Barriers

Particular Set of Skills *Hardware*

ultra-low-power circuits, breaking traditional rules, repurposing components *Software*

scant memory, energy constraints

Research-grade Tools/ Platforms

Ekho, Capybara, Mayfly, Alpaca, SONIC, Flicker, Zygarde

Standards/Benchmarks

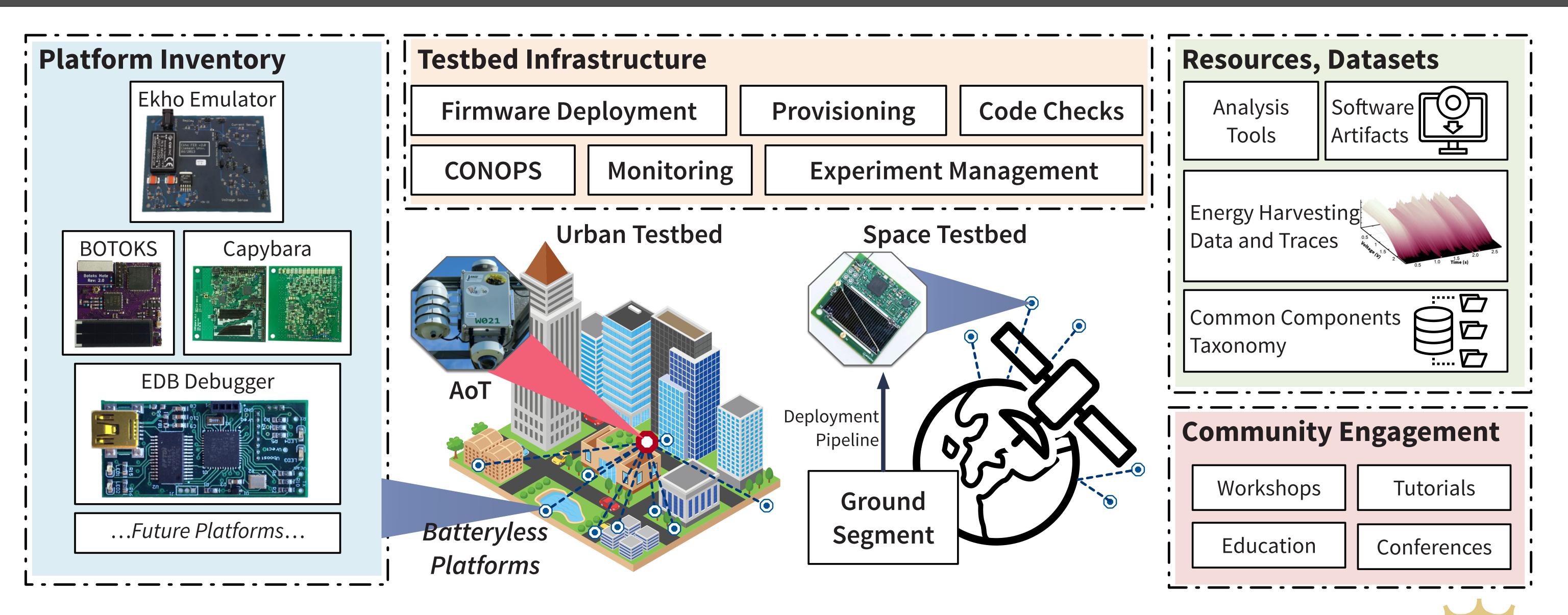
energy traces and modelstest programs,reference circuits,common components,lessons learned the hard way

How do I...

determine the right capacitor size? emulate an I-V surface? adjust power supply hysteresis? use a comparator for charging? deal with HW and SW heisenbugs?

No commercial hardware!

Fundamental Infrastructure



https://persist.cs.clemson.edu

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